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2004

520.41287X00

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Applicants: T. OSABE, et al.  
Application No.: 10/082,205  
Filed: February 26, 2002  
For: SEMICONDUCTOR MEMORY ELEMENT,  
SEMICONDUCTOR DEVICE AND CONTROL  
METHOD THEREOF  
Art Unit: 2811  
Examiner: T. F. Tran

**AMENDMENT**

September 15, 2003

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

In response to the Office Action dated March 13, 2003, the period of response for which extension is requested by the attached Petition for Extension of Time, please amend the above-identified application as listed below and as set forth on the following pages:

**Amendments to Claims and****Remarks are included following the amendments.**

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Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450  
Attention: Examiner T. F. Tran  
Art Unit: 2811

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SEP 16 2003

Re: T. OSABE, et al. – Application No.: 10/082,205  
Our Ref. No.: 520.41287X00

**SUBMISSION OF DOCUMENTS**

**OFFICIAL**

Sir:

Applicants hereby submit the attached "Amendment" (9 pgs), "Extension of Time (1 pg) and Credit Card Payment Form (1 pg) in the above-identified application.

**CERTIFICATE OF TRANSMISSION:**

I hereby certify that the attached "Amendment" (9 pgs), Extension of Time (1 pg) and Credit Card Payment Form (1 pg), are being formally filed in the USPTO via Group 2800 Facsimile No. 703-872-9318 on September 15, 2003.

  
Deborah L. Therrien

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